

A Refinement Process for Top-Down Mixed-Signal Designs

Thanks to SystemC-AMS

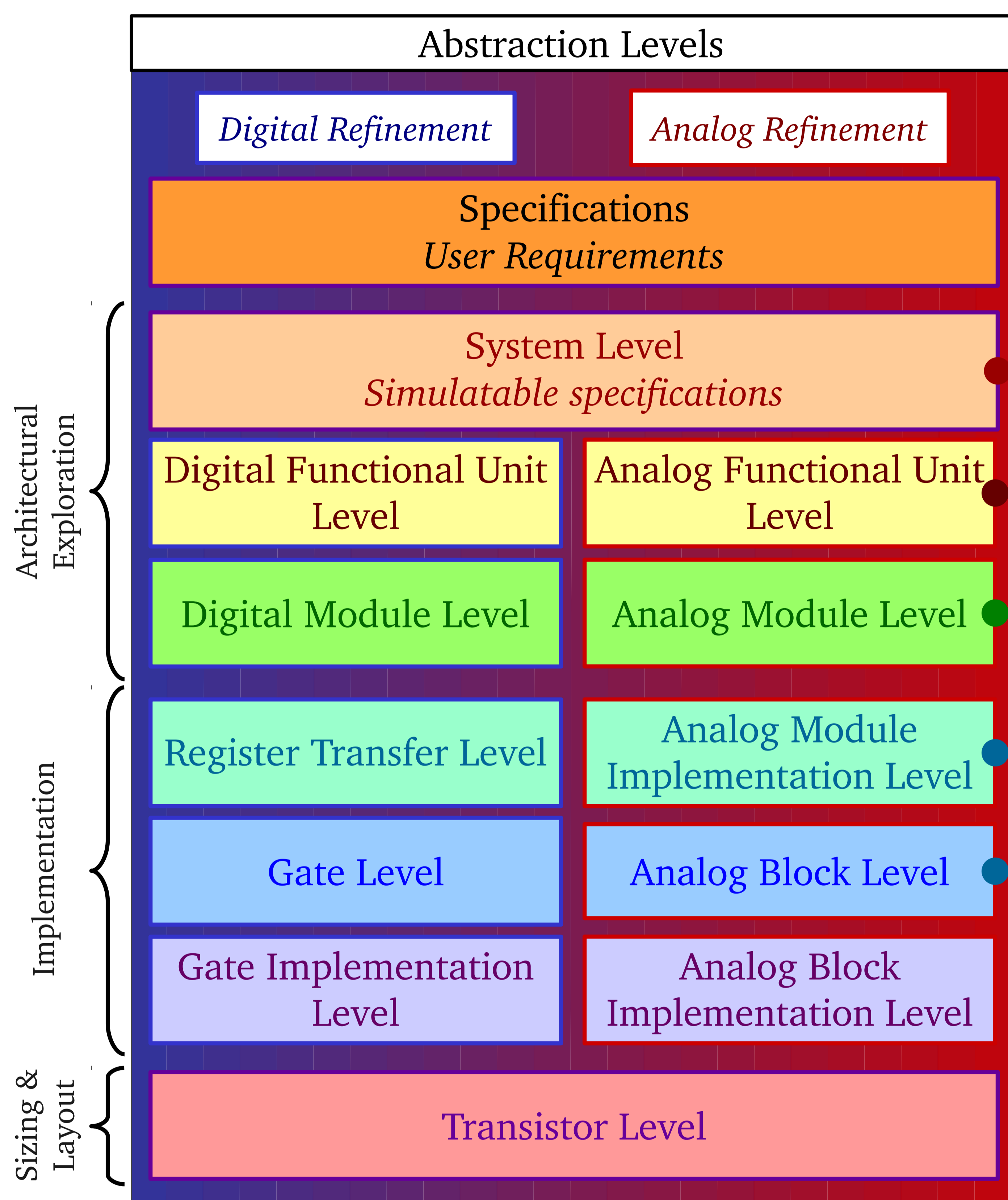


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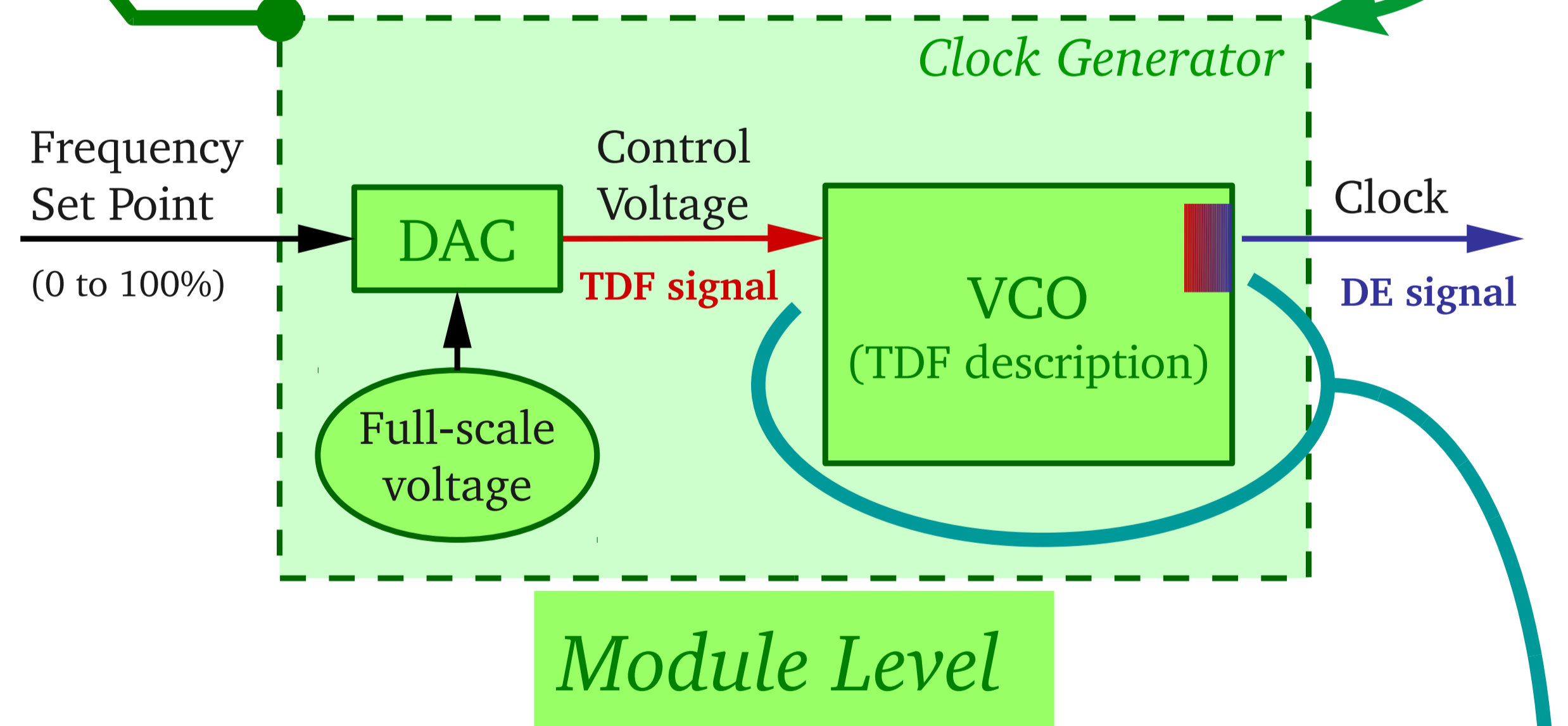
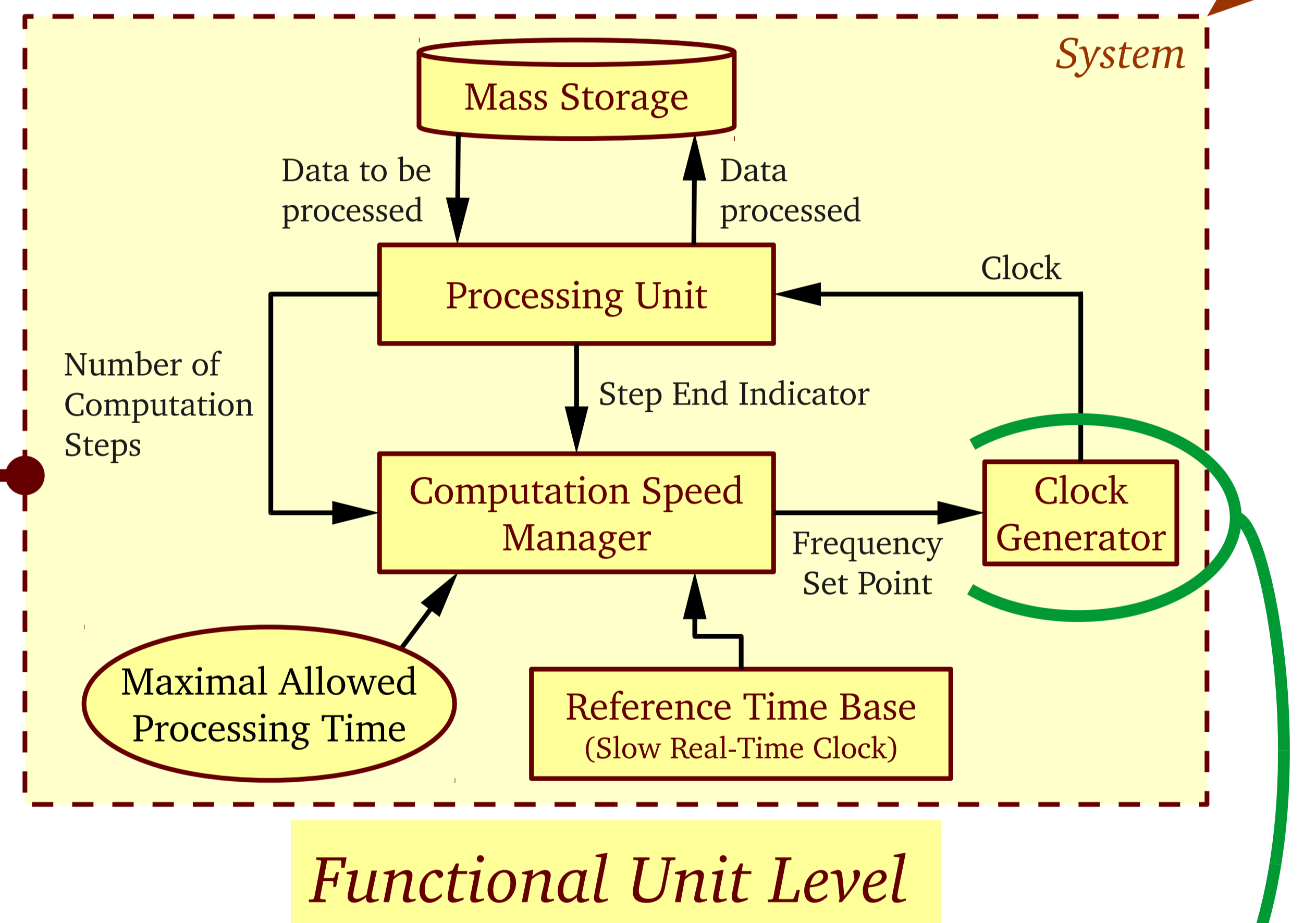
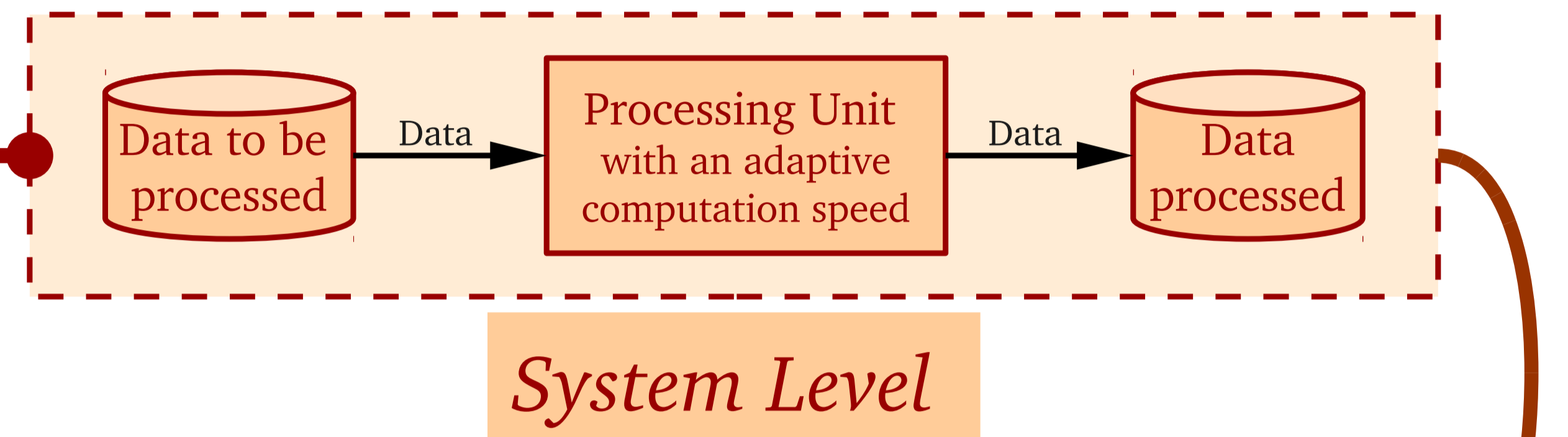
A top-down refinement process for mixed-signal designs suitable to:

- cosimulate the analog and digital parts
- manage the interfaces between the analog and digital parts
- take into account the analog effects of the digital domain



Example

Refinement of a processing unit that continuously adapts its computational frequency in order to optimize the electrical power while meeting the system deadlines



→ sc_signal
→ sca_tdf::sca_signal
→ other signals or data

Implementation Level

